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Fig.1

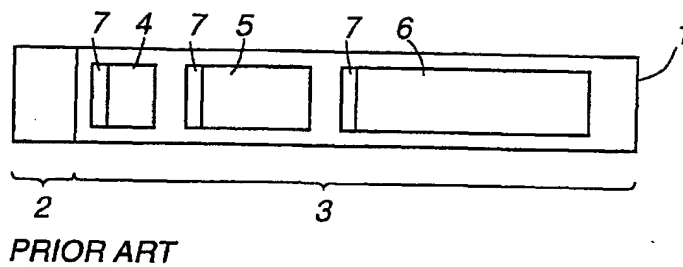


Fig.2

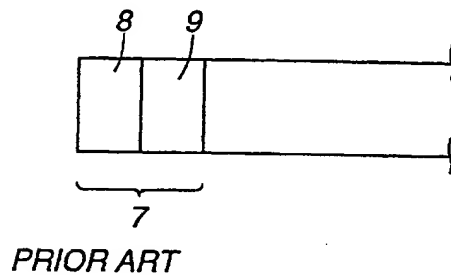


Fig.3

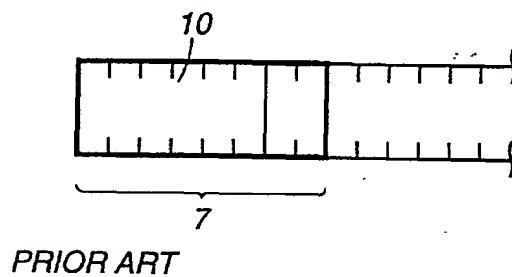
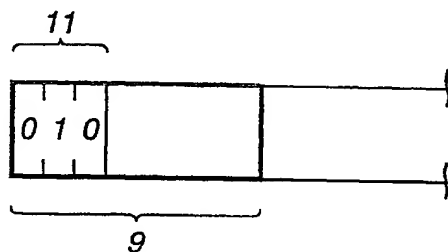


Fig.4



# REPLACEMENT SHEET

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Fig.5

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CODE	SIZE (OCTETS)
0 0 0	4
0 0 1	8
0 1 0	16
0 1 1	20
1 0 0	35
1 0 1	43
1 1 0	56
1 1 1	60

Fig.6

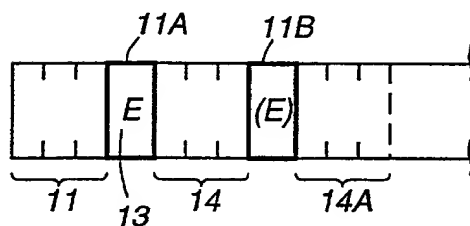


Fig.7

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CODE	SIZE (OCTETS)
0 0 0 0 0 0	2
0 0 0 0 0 1	4
0 0 0 0 1 0	5

1 1 1 1 0 0	100
1 1 1 1 0 1	125
1 1 1 1 1 0	150
1 1 1 1 1 1	200

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Fig.8

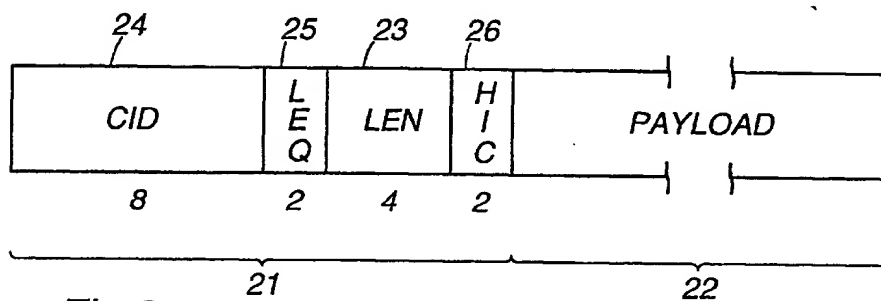
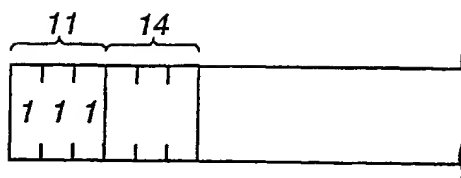


Fig.9

Fig.10

LEQ	LEN
0 0	0 0 0 0
0 0	0 0 0 1
0 0	0 0 1 0
.	.
0 0	1 1 1 1
0 1	0 0 0 0
0 1	0 0 0 1
.	0 0 1 0
.	.
0 1	1 1 1 1
1 0	0 0 0 0
.	0 0 0 1
.	0 0 1 0
.	.
1 0	1 1 1 1

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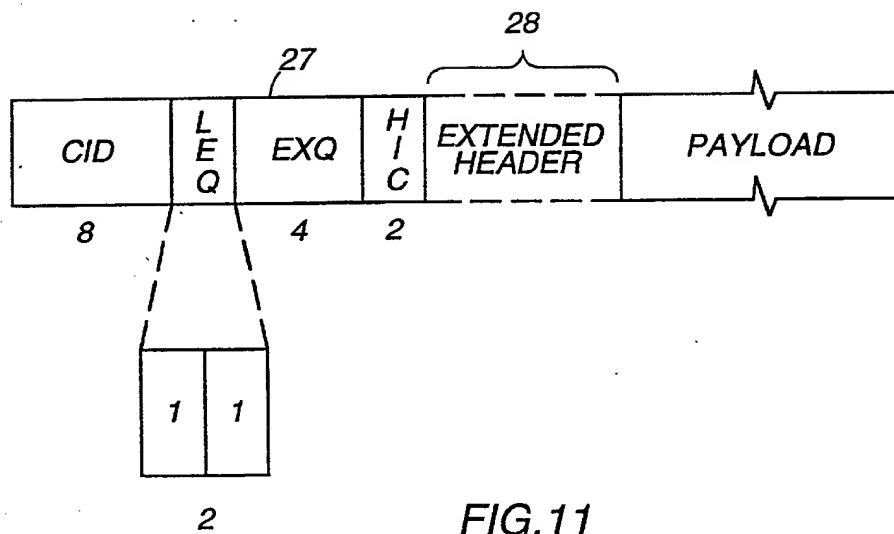
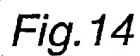
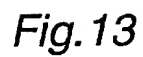
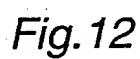


FIG.11



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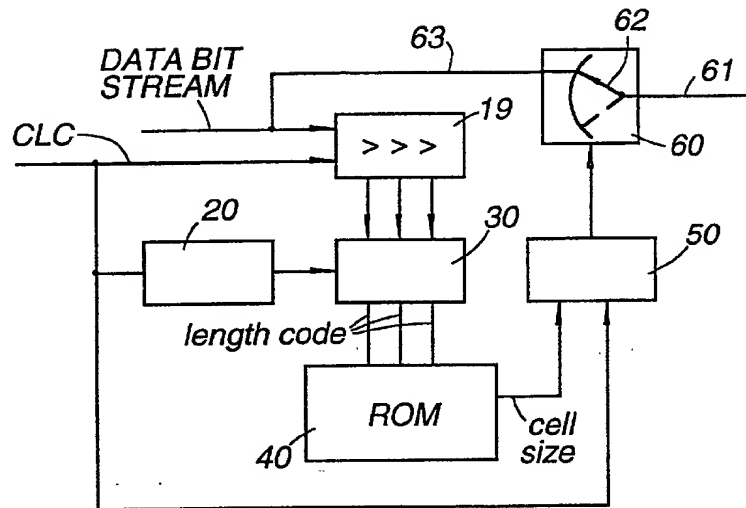


Fig.15

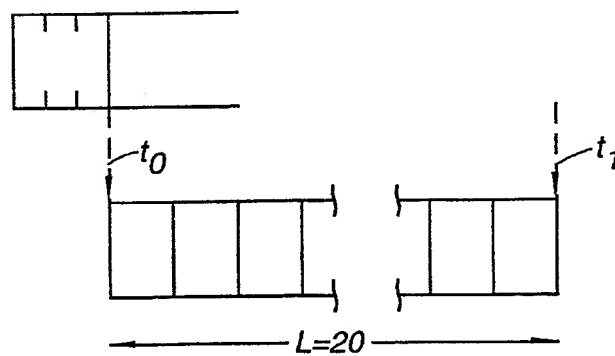


Fig.16

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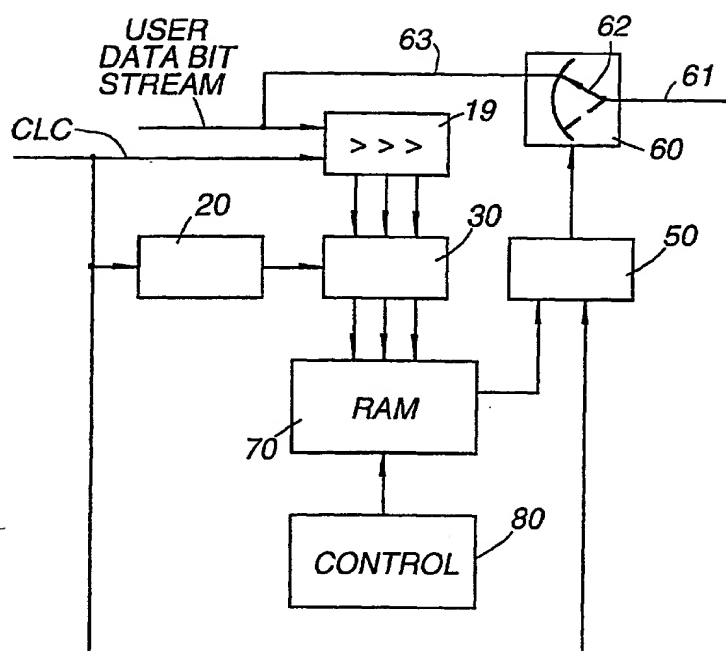


Fig.17

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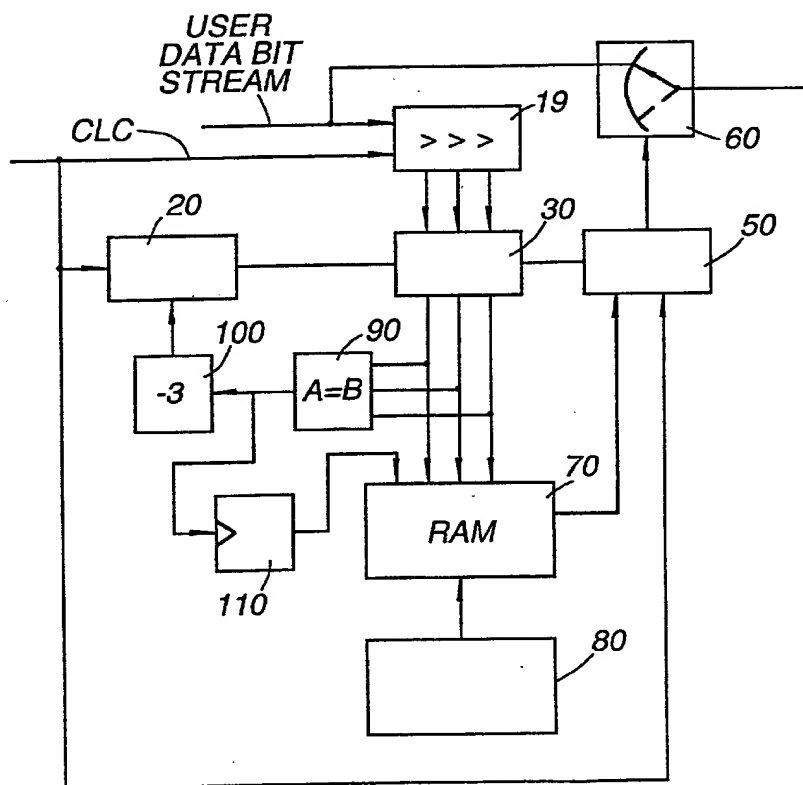


Fig.18



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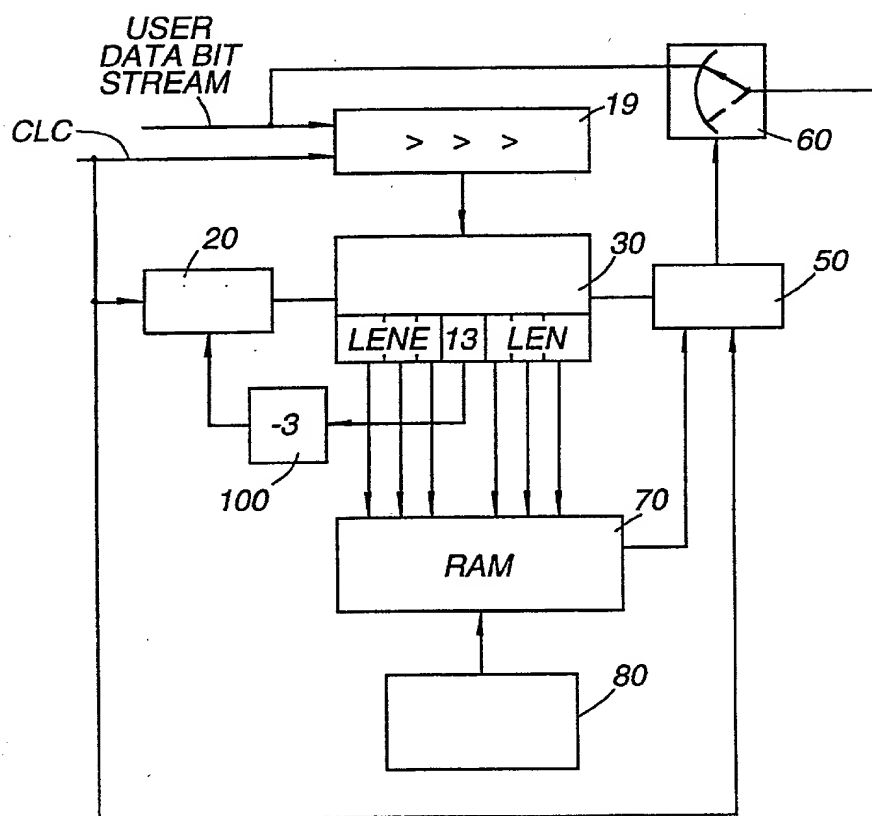


Fig.19

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Fig.20

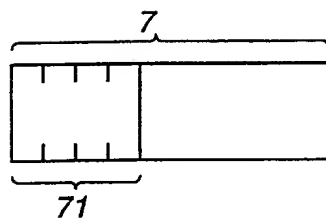
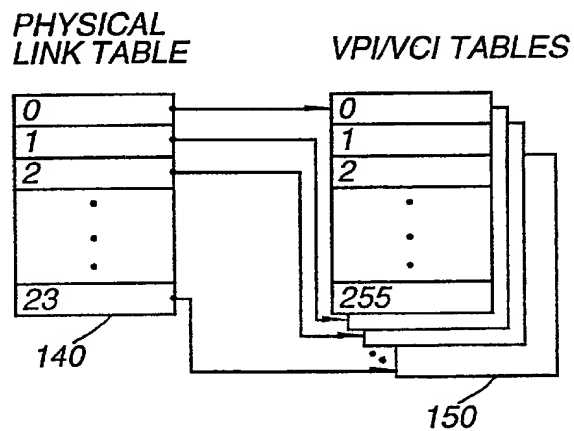


Fig.21

CID	SIZE
0 0 1	2
0 1 0	2
0 1 1	4

Fig.22



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Fig.23

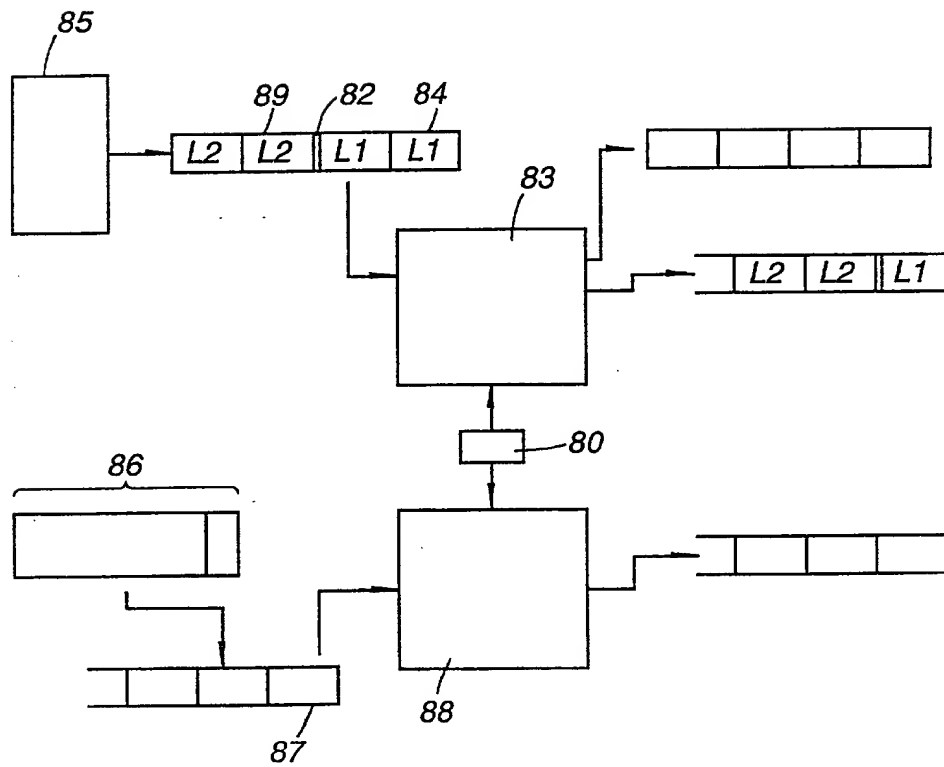
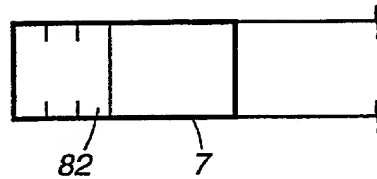


Fig.29

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Fig.24

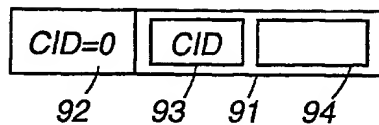


Fig.25

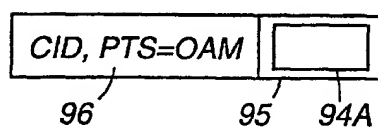


Fig.26

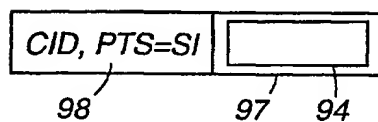


Fig.27

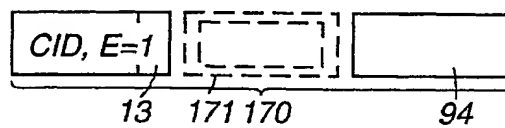


Fig.28



Timing diagram showing data transfer between three processors (100, 101, 105) over time. The diagram illustrates the sequence of data transfers and the state of the C-PLANE (indicated by double lines).

- Processor 100:** Initiates data transfer to 101.
  - Transfer 1: (LEN=15)
  - Transfer 2: (LEN=15)
  - Transfer 3: (NEW LEN=23)
  - Transfer 4: (NEW LEN=23)
- Processor 101:** Receives data from 100 and transfers it to 105.
  - Transfer 1: (LEN=15)
  - Transfer 2: (LEN=15)
  - Transfer 3: (NEW LEN=23)
  - Transfer 4: (LEN=15)
  - Transfer 5: (FLAG, LEN=23)
  - Transfer 6: (FLAG, LEN=23)
  - Transfer 7: (LEN=23)
  - Transfer 8: (LEN=23)
- Processor 105:** Receives data from 101.
  - Transfer 1: (LEN=15)
  - Transfer 2: (LEN=15)
  - Transfer 3: (NEW LEN=23)
  - Transfer 4: (LEN=15)
  - Transfer 5: (FLAG, LEN=23)
  - Transfer 6: (FLAG, LEN=23)
  - Transfer 7: (LEN=23)
  - Transfer 8: (LEN=23)

The C-PLANE is active (indicated by double lines) during the first four transfers from 100 to 101 and the first four transfers from 101 to 105. It becomes inactive (single line) after the fifth transfer from 101 to 105.

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Fig.32

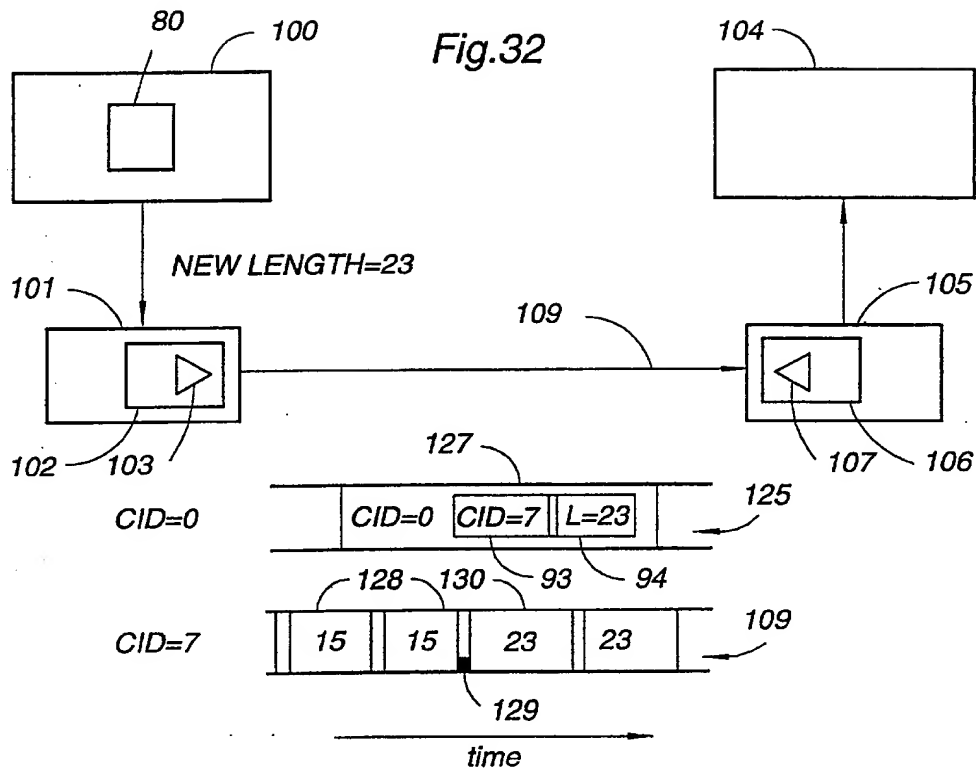
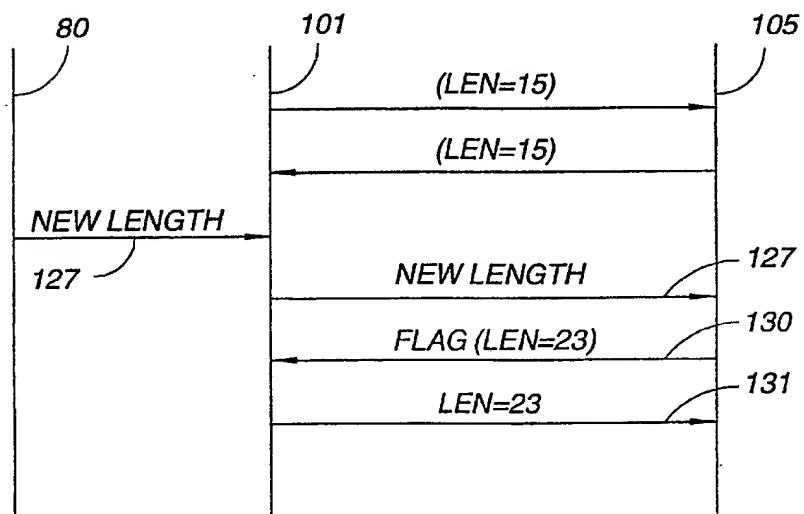


Fig.33



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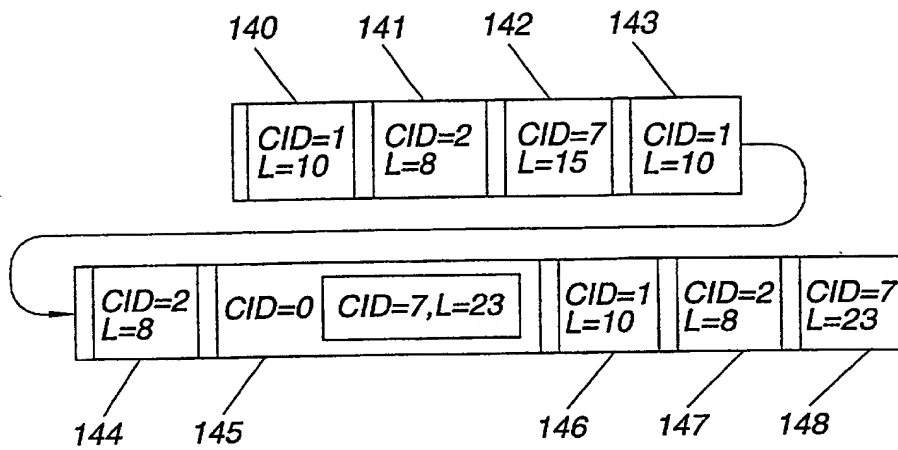


Fig.34

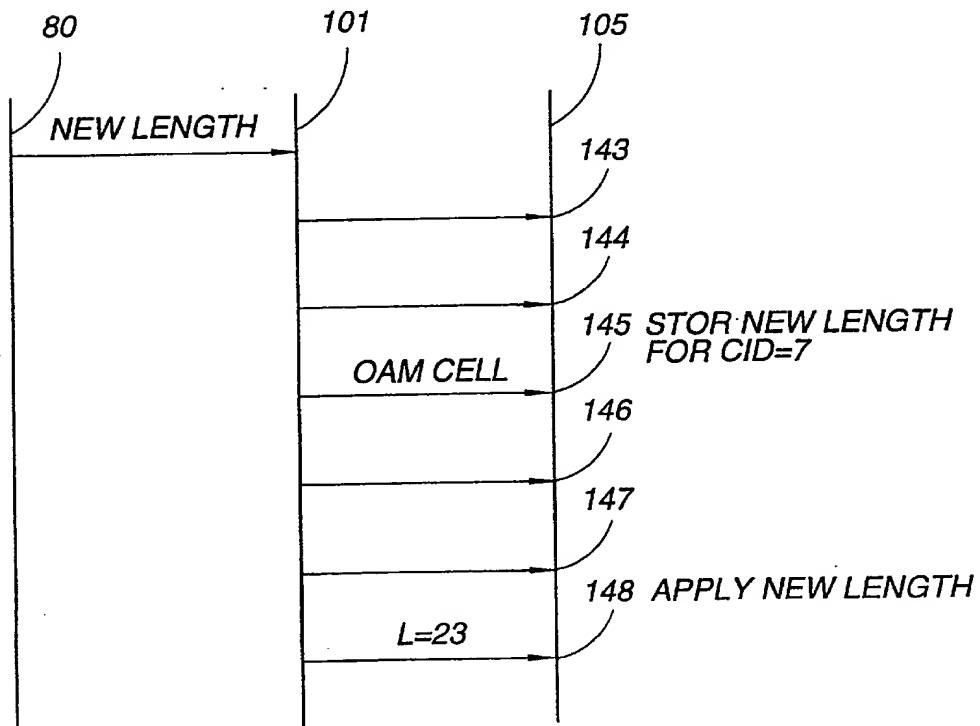


Fig.35

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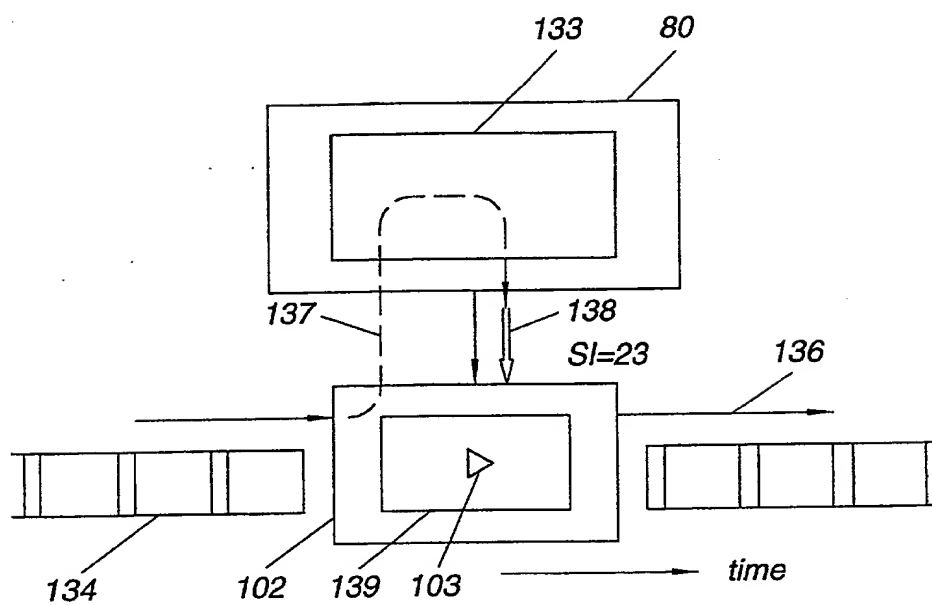


Fig.36



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Fig.37

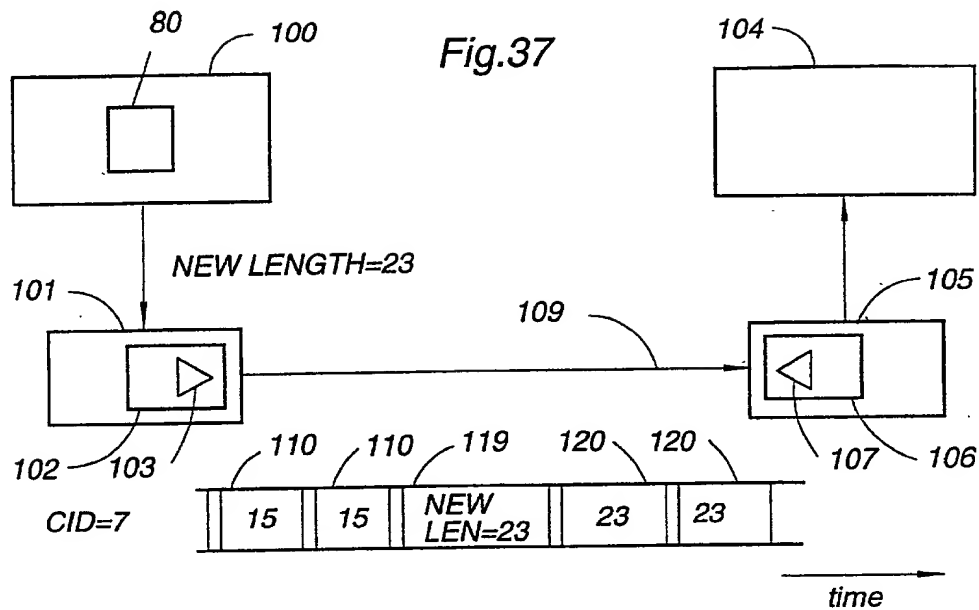
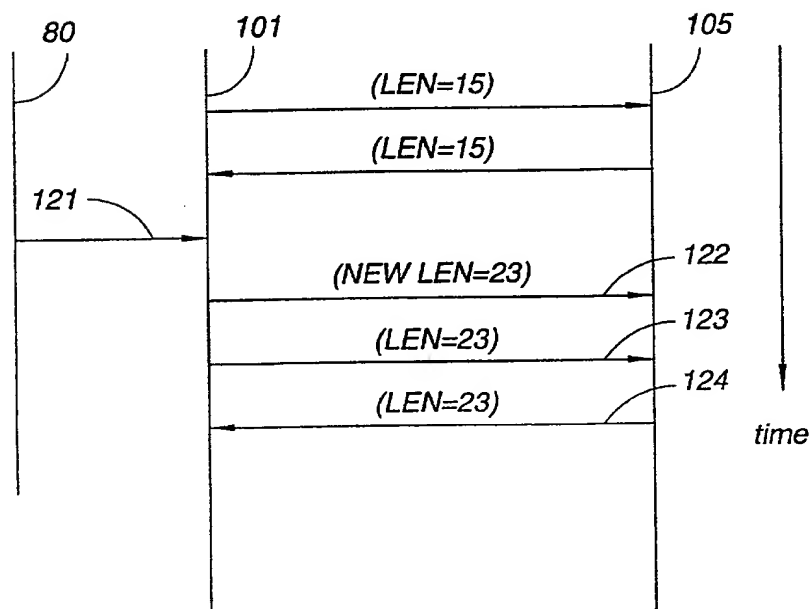


Fig.38



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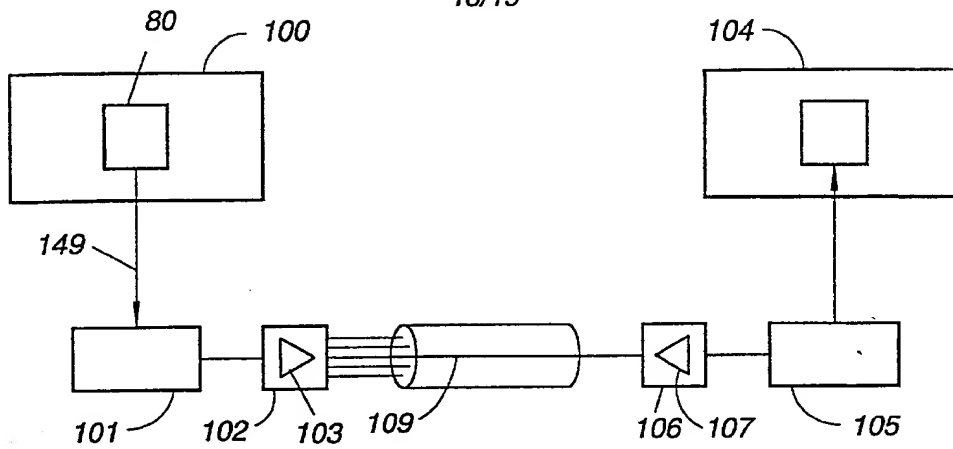


Fig. 39

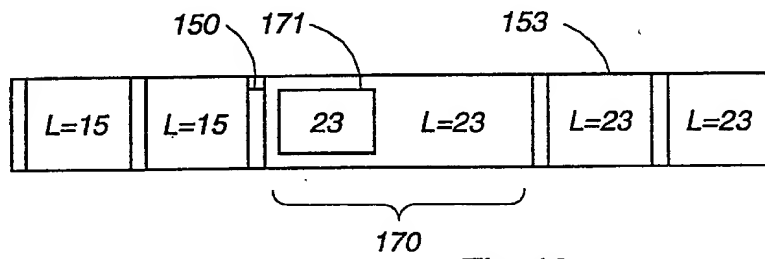


Fig. 40

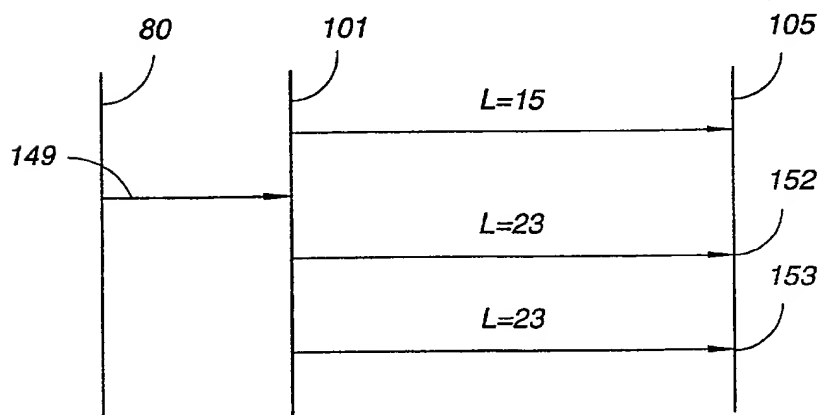


Fig. 41

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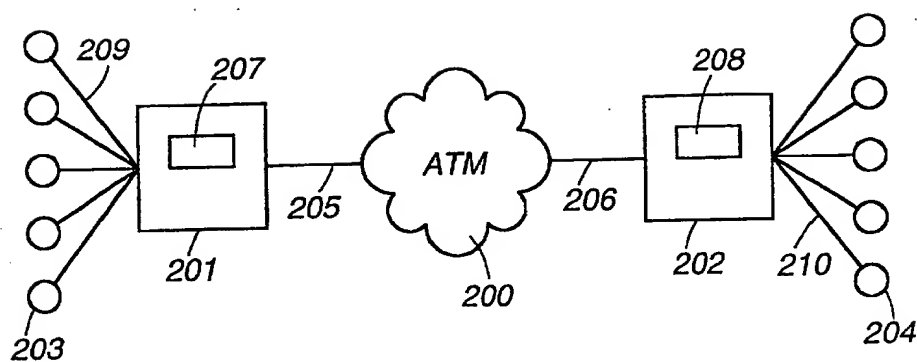


Fig.42